

We claim:

1. A digital broadcast receiver comprising:

a demultiplexer for separating a digital broadcast signal in which a compressed video signal, a compressed audio signal, and a data signal in association with the compressed video and audio signals are multiplexed;

a decoder buffer for storing the compressed video signal and the compressed audio signal separated by said demultiplexer;

a video decoder for decoding said compressed video signal in said decoder buffer;

an audio decoder for decoding said compressed audio signal in said decoder buffer;

a memory for storing said data signal separated by said demultiplexer; and

a CPU for analyzing the data signal stored in said memory;

wherein said CPU allows the data signal analyzed by said CPU to be stored in said decoder buffer when the analyzed data signal includes a compressed video signal or a compressed audio signal and the compression method used for the compressed video signal or the compressed audio signal is the same as that used for said compressed video signal or said compressed audio signal multiplexed in the

digital broadcast signal.

2. A digital broadcast receiver as claimed in claim 1, wherein said CPU decodes analyzed data when the content of the analyzed data signal is a data signal which is not compressed by the same method as that used for the compressed video signal or the compressed audio signal multiplexed in the digital broadcast signal.

3. A digital broadcast receiver as claimed in claim 1, wherein said decoder buffer possesses, by time division, the compressed video signal and the compressed audio signal separated by said demultiplexer as well as the compressed video signal and the compressed audio signal included in said data signal, and uses each of the signals in an exclusive manner.

4. A digital broadcast receiver as claimed in claim 1, wherein said video decoder and said audio decoder retain a write address used when the compressed video signal and the compressed audio signal included in said data signal are stored in said decoder buffer, and from the difference between the write address and a read address for said video decoder and said audio decoder to read said decoder buffer, senses whether the compressed video signal and the compressed audio signal accumulated in said decoder buffer are depleted or not, or senses the progress of decoding, whereby said video decoder and said audio decoder stop,

resume, or repeat decoding.

5. A digital broadcast receiver as claimed in claim 1, wherein said video decoder and said audio decoder produce an interrupt signal for said CPU according to whether the compressed video signal and the compressed audio signal accumulated in said decoder buffer are depleted or not, or according to the progress of decoding, whereby said CPU controls decoding in response to said interrupt signal.

6. A digital broadcast receiver as claimed in claim 1, wherein said video decoder and said audio decoder produce a frame pulse interrupt signal for said CPU, whereby said CPU receives said frame pulse interrupt signal and controls decoding by counting the number of frames related to the frames of said compressed video signal and said compressed audio signal.

7. A digital broadcast receiver comprising:

a demultiplexer for separating a digital broadcast signal in which a compressed video signal, a compressed audio signal, and a data signal in association with the compressed video and audio signals are multiplexed;

a decoder buffer for storing the compressed video signal and the compressed audio signal separated by said demultiplexer;

a video decoder for decoding said compressed video

signal in said decoder buffer;

a display for displaying a video signal decoded by said video decoder;

an audio decoder for decoding said compressed audio signal in said decoder buffer;

a speaker for outputting an audio signal decoded by said audio decoder;

a memory for storing said data signal separated by said demultiplexer; and

a CPU for analyzing the data signal stored in said memory;

wherein said CPU allows the data signal analyzed by said CPU to be stored in said decoder buffer when the analyzed data signal includes a compressed video signal or a compressed audio signal and the compression method used for the compressed video signal or the compressed audio signal is the same as that used for said compressed video signal or said compressed audio signal multiplexed in the digital broadcast signal.

8. A CPU for use in receiving and processing a digital broadcast signal in which a compressed video signal, a compressed audio signal, and a data signal in association with the compressed video and audio signals are multiplexed,

wherein said CPU analyzes whether or not a compressed video signal or a compressed audio signal is included in

said data signal, and whether the compression method used for the compressed video signal or the compressed audio signal is the same as that used for said compressed video signal or said compressed audio signal multiplexed in the digital broadcast signal; and

based on the result of the analysis, said CPU changes the destination to which the data signal is outputted.

9. A decoder for use in receiving and processing a digital broadcast signal in which a compressed video signal, a compressed audio signal, and a data signal in association with the compressed video and audio signals are multiplexed, comprising:

a decoder buffer for storing said compressed video signal and said compressed audio signal;

a video decoder for decoding said compressed video signal in said decoder buffer;

an audio decoder for decoding said compressed audio signal in said decoder buffer; and

a CPU for analyzing said data signal,

wherein said CPU allows the data signal analyzed by said CPU to be stored in said decoder buffer when the analyzed data signal includes a compressed video signal or a compressed audio signal and the compression method used for the compressed video signal or the compressed audio

signal is the same as that used for said compressed video signal or said compressed audio signal multiplexed in the digital broadcast signal.